BB Interface pins Matter!

Common Analog IQ IF

- 12 pins for IQ Traffic
- 4 pins for SPI Control
- 2 pins for Strobes
- 4 pins for line control (Gain)

22 pins minimum (with no Front End Control)

- Plus -

- 12 pins for Front End Control

Total BB Pins

34 pins (Including RF Transceiver Control)

DigRF4G V1.0 IF
(with Fujitsu RF Transceivers)

- 2 pins for Transmit
- 4 pins for Receive
- 1 pin for Reference Clock
- 1 pin for System Clock
- 1 pin for Reference Enable

9 Total BB Pins

Fujitsu Transceiver Controls
RF Front End
(PAs, DCDC Regulators, Power Detectors, Antennae Module, Switches)