This high-frequency current-noise-measurement circuit emulates a photodiode transimpedance amplifier in which $C_2$ replaces the photodiode. $R_1$ and $C_1$ enable high-frequency gain calibration. $C_1$ is a parallel combination to minimize parasitic inductance.

**Figure E** This high-frequency current-noise-measurement circuit emulates a photodiode transimpedance amplifier in which $C_2$ replaces the photodiode. $R_1$ and $C_1$ enable high-frequency gain calibration. $C_1$ is a parallel combination to minimize parasitic inductance.