Trace delay provides 75% of the feedback delay.

The time delay between Y0 and CLOCK RECEIVER is 1.2 nSEC. 75% (0.9 nSEC) of the feedback delay is from the trace delay. Second pair of traces at a different length for spare option.

25% (0.3 nSEC) of the feedback delay is from the RC circuit of R1, R2, and C1.