A short or low-frequency interconnect looks like a pair of conductors and a lumped capacitance. The resistance of a conductor on a pc board is usually too small to consider, but you should include the driver’s output resistance in calculating the time constant.

NOTE: PROPAGATION DELAY IS THE CHARGING TIME FOR THE CAPACITOR, CALCULATED BY $V = V_0(1 - e^{-t/RC})$. 

Figure 3